

JC885 U.S. PTO
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Date: June 27, 2001

[illegible]

Transmitted herewith for filing under 37 C.F.R. §1.53(b) is the patent application of

Inventor(s): Marc HOURDEQUIN, Luille, France;
Christian LAGORCE, Pance, France; and
Laurent MALAQUIN, Chavagne, France.

For: LOGIC CIRCUIT DEVICE FOR GENERATION OF A VARIABLE SIGNAL

Enclosed are:

- ☒ Two (2) sheets of drawings, Figures 1-3.
- ☐ An Assignment of the invention to DELEGATION GENERALE POUR L'ARMEMENT, Armees, France.
- ☐ Certified copy of French application No. 00.08241, filed June 27, 2000.
- ☐ A Declaration and Power of Attorney.
- ☒ This application is not entitled to small entity status.
- ☐ A Preliminary Amendment is enclosed.
- ☐ An Information Disclosure Statement.
- ☒ The Verified English-Language Translation, executed Declaration and Assignment and full filing fees will follow in due course.

	Number Filed	Number Extra	Rate	Basic Fee \$710.00
Total				
Claims	11	- 20 =	x \$ 18.00=	\$
Independent				
Claims		- 3 =	x \$ 80.00=	\$
Multiple Dependent Claim(s)				
(if applicable)			+ \$ 270.00=	\$
Total				0.00
Reduction by 1/2 for filing Small Entity				
Assignment recordation fee			\$ 40.00=	\$
TOTAL FILING FEE				\$0.00

- [] Our Check No. _____ in the amount of the total filing fee is enclosed. However, if the check is missing or insufficient, the Commissioner is authorized to charge any additional fees which may be required, or credit any overpayment, to our Deposit Account No. _____.
- [] The Commissioner is hereby authorized to charge payment of the following fees during the pendency of this application or credit any overpayment to our Deposit Account No. _____: any patent application processing fees under 37 CFR 1.17; and any filing fees under 37 CFR 1.16 for presentation of extra claims.

Respectfully submitted,

PARKHURST & WENDEL, L.L.P.



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